

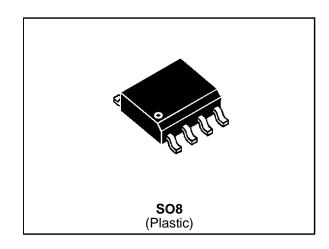
DA112S1

Application Specific Discretes A.S.D.TM

DIODE ARRAY

FEATURES

- ARRAY OF 12 SURGE-RATED DIODES
- VERY LOW CAPACITANCE
- SUITABLE FOR HIGH-SPEED TRANSMISSION

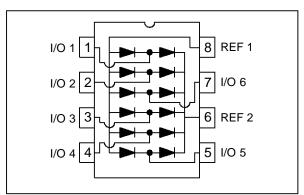


DESCRIPTION

Array of diodes configured to clamp six signals to a fixed reference so as to prevent damage caused by overvoltages. The reference can be either the supply rails or a Transil_{TM} clamping device.

The diode array can be used for protecting 2 twisted-pair cables, e.g. a 10-BASE-T driver and receiver. If necessary, the diodes can be put in parallel in order to increase surge capability.

FUNCTIONAL DIAGRAM



ABSOLUTE MAXIMUM RATINGS $(0^{\circ}C \le T_{amb} \le 70^{\circ}C)$

Symbol	Parameter		Value	Unit
VRRM	Repetitive peak reverse voltage (for one single diode)		18	V
I _{PP}	Repetitive peak forward current *	8/20 μs	12	Α
P _{tot}	Power dissipation	T _{amb} = 25℃	0.73	W
Tstg Tj	Storage temperature range Maximum junction temperature		- 55 to + 150 150	သိ

^{(*} The surge is repeated after the device returns to thermal equilibrium)

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
R _{th (j-a)}	Junction to ambient	170	°C/W
R _{th (j-l)}	Junction to leads	60	°C/W

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ELECTRICAL CHARACTERISTICS $(T_{amb} = 25^{\circ}C)$

Symbol	Parameter		Max.	Unit
V_{FP}	Peak voltage	$I_{PP} = 12A, 8/20 \mu s$	9	V
V_{F}	Formard voltage	$I_F = 50 \text{ mA}$	1.2	V
I_R	Reverse leakage current	$V_R = 15V$	2	μΑ

Fig.1: Input capacitance

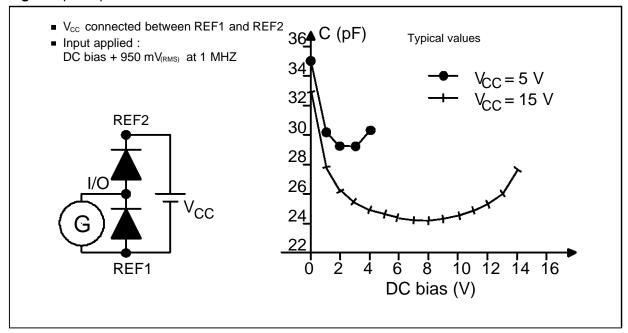
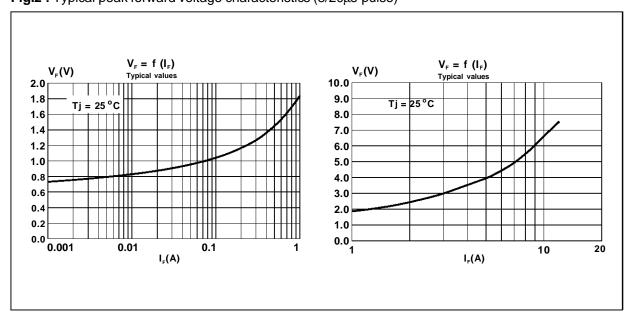
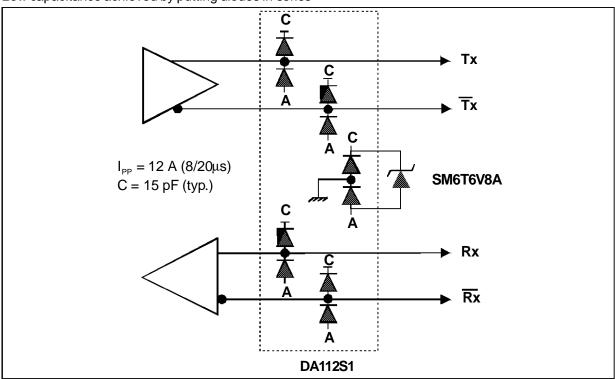


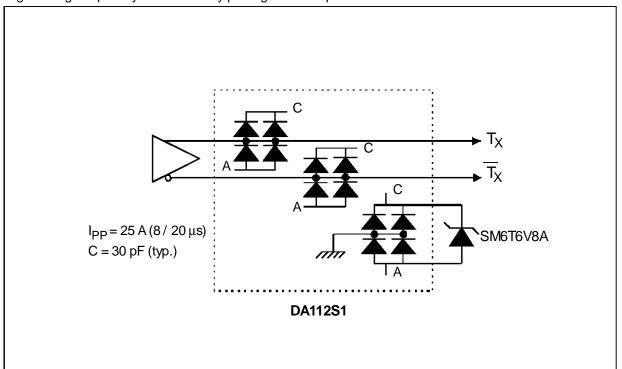
Fig.2: Typical peak forward voltage characteristics (8/20µs pulse)



APPLICATION 1: 10-BASE-T Interface Protection Low capacitance achieved by putting diodes in series

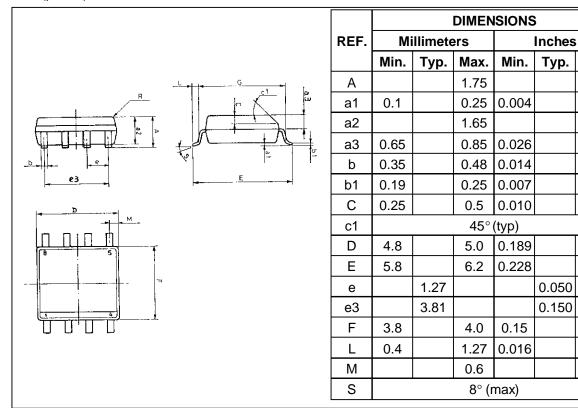


APPLICATION 2 : Differential driver protection Higher surge capability is achieved by putting diodes in parallel.



PACKAGE MECHANICAL DATA

SO8 (plastic)



Max.

0.069

0.010

0.065

0.033

0.019

0.010

0.020

0.197 0.244

0.157

0.050

0.024

Packaging: Products supplied in antistatic tubes.

MARKING: Logo, Data Code, DA112S

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